



Docket No.: AMI 99 0006

Patent Application

4-5-00

A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Application of:

Allan Havemose

Entitled: Dynamic Compiling

April 4, 2000

To the Assistant Commissioner
for Patents
Box Patent Application
Washington, D.C. 20231

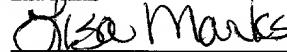
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Dear Sir:

REQUEST FOR FILING A NATIONAL PATENT APPLICATION

Transmitted herewith for filing, please find the following:

- 1. Specification, claims and abstract of the above-referenced patent application having 24 pages.
- 2. 12 sheet(s) of drawing(s) (X formal / informal) comprising Figures 1 through 13.
- 3. Combined Declaration and Power of Attorney (X signed unsigned).
 - 3A. No filing fee, Oath, or Declaration is enclosed pursuant to 37 C.F.R 1.53(d).
- 4. Information Disclosure Statement along with Form PTO-1449 and references.
- 5. This is a Continuation-In-Part of Application Serial No.60/127,767 filed April 5, 1999 and Application Serial No. 09/312,123 filed May 14, 1999.

An extension to extend the life of the above prior Application to at least the date of filing hereof

(One box must be marked)

- (a) is concurrently being filed in that prior Application,
(b) was previously filed in that prior Application,
(c) is not necessary for copendency.

6. Attached is an assignment to **Gateway, Inc.** Please return the recorded assignment to the undersigned.

7. Priority is claimed under 35 U.S.C. § 119 based on filing in European Patent Office.

	<u>Application No.</u>	<u>Filing Date</u>
(1)	_____	_____
(2)	_____	_____
(3)	_____	_____

(No.) Certified copy (copies) are attached; or were previously filed on .

8. Attached: (No.) verified statement(s) establishing "small entity" status under 37 CFR § 1.9 and 1.27.

9. Attached:

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 (Other)

10. Preliminary Amendment:

Prior to a first Office Action, kindly amend the Application as follows:

11. The following Filing Fee calculation is based on the claims filed less any claims canceled by the Preliminary Amendment of Item 10.

				SMALL ENTITY RATE		LARGE ENTITY RATE	
BASIC FEE				\$345	<u>OR</u>	\$690	= \$690.00
	NUMBER FILED		NUMBER EXTRA				
TOTAL CLAIMS	20	-20	= 0 (at least 0)	x 9	<u>OR</u>	x 18	= +\$0.00
INDEP. CLAIMS	4	- 3	= 1 (at least 0)	x 39	<u>OR</u>	x 78	= +\$78.00
If any <u>proper</u> multiple dependent claim (ignore improper) is present (Enter \$0.00 if this is a <u>reissue</u> application.)				+\$130	<u>OR</u>	+\$260	= +\$0.00
If assignment is x'd (item 6), add recording fee \$40.00							+\$40.00
Attached is a Rule 47 Petition (inventor refuses to sign or cannot be reached) \$130							+\$0.00
TOTAL FILING FEE							= \$808.00

12. A check in the amount of \$____ to cover the Filing Fee calculated in Item 11 is attached. Please charge any deficiency or credit any overpayment to Deposit Account No. 50-0439.
- X 13. Please charge my Deposit Account No. 50-0439 in the amount of \$808.00 to cover the Filing Fee calculated in Item 11. This sheet is attached in duplicate.
- X 14. The Commissioner is hereby authorized to charge any fee specifically authorized hereafter, or any missing or insufficient fee(s) filed, or asserted to be filed, or which should have been filed herewith or concerning any paper filed hereafter, and may be required under 37 CFR 1.16-1.18 (missing or insufficiencies only) now or hereafter relative to this application and for the resulting Official Document under 37 CFR 1.20, and to have and cause any necessary petition for extension of time to be filed and any fees necessary to be paid for said extension of time OR credit any overpayment to our Deposit Account No. 50-0439, for which purpose a duplicate copy of this sheet is attached. **The Commissioner is not authorized to charge the issue fee until/unless an issue fee transmittal form is filed.**

Docket No.: AMI 99 0006

Patent Application

DATED: April 4, 2000

Respectfully submitted,
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION FOR PATENT

FOR

DYNAMIC COMPILING

BY

ALLAN HAVEMOSE

AMI 99-0006

DYNAMIC COMPILING

Cross Reference to Related Applications

The present application claims the benefit under 35 U.S.C. §119(e) of United States Provisional Patent Application Serial Number 60/127,767 filed on April 5, 1999. Said United States Provisional Application 60/127,767 is herein incorporated by reference in its entirety. The present application also claims the benefit under 35 U.S.C. §120 of United States Patent Application Serial Number 09/312,123, filed May 14, 1999, pending. Said United States Application 09/312,123 is herein incorporated by reference in its entirety. The present application also incorporates the following applications by reference in their entirety:

<i>Attorney Docket Number</i>	<i>Filing Date</i>	<i>Serial Number</i>
AMI 99-0002		EL 533 974 913 US
AMI 99-0003		EL 533 974 927 US
AMI 99-0004		EL 533 974 935 US
AMI 99-0005		EL 533 974 944 US

Field of the Invention

The present invention relates generally to the fields of data manipulation and more particularly to a system and method of dynamic compiling.

5

Background of the Invention

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Methods and apparatus for transacting business over a network are old in the art. For example, telephone communications have long been utilized to transact purchases and transfer funds between accounts. Likewise, current cable and satellite television systems allow viewers to order video and audio content paid for via a viewer's credit or debit account information. Additionally, "on-line" purchases of goods and services are becoming common over the INTERNET. However, such methods and apparatus do not allow a buyer and a seller to transact business utilizing a common or universal transaction system.

5 Digital information appliances (DIA) are electronic devices designed to perform a specific function or group of functions more efficiently than would a conventional computer system. Like computer systems, information appliances may be interconnected with a network such as the INTERNET to provide content and functions which would not be available when the appliances operated independently. Preferably, such network connections are transparent to the user so that the complexity of the underlying computer network is masked. In this manner, information appliances provide advantages in simplicity of operation and computing ease of use to their users.

10 As the proliferation of digital information appliances accelerates, it will become necessary to develop a standard system architecture and operating environment to facilitate their use and interconnection with each other and other networked devices. Such a system architecture may utilize a distributed object model employing object oriented programming methods. Object oriented programming is a programming paradigm (method) wherein a program is organized as a collection of discrete objects that are self-contained collections of data structures and routines that interact with that data. Such objects encapsulate related data and procedures so as to hide that information by allowing access to the data and procedures only through the object's published interface. Hence changes to the data and or procedures of the object are isolated from other objects. This provides an architecture that is more easily maintained since changes to an object's code does not affect other objects.

15 Likewise, object oriented programming methods provide for inheritance of an object's characteristics into another class of object. Thus, an object may be derived from a first object to form a second object which "inherits" certain properties of its parent object. This allows for both (1) the formation of subclasses of objects having more specialized features and/or capabilities, and (2) the reuse of individual objects in different programs. 20 Thus, libraries of proven objects may be developed which may be used repeatedly in different applications.

25 In developing a standard appliance system architecture, it is desirable to allow access to objects in a transparent fashion so that objects created in different programming languages

and objects residing on different appliances, network servers, or computer systems that are networked together are accessible to the user without extensive modification of the user's programming code. For computer networks, this capability may be provided by object oriented distributed environments such as the common object request broker architecture (CORBA). Such system architectures are based upon a client-server model, in which object servers provide public interfaces to object-clients that make requests of the object servers. Typically in such systems, the servers are objects consisting of data and associated methods. The object clients obtain access to the object servers by sending them messages which are mediated by the distributed system. When the server object receives the message it invokes the appropriate method and transmits the result back to the object client. The object-client and object server communicate through an Object Request Broker (ORB) which is used to locate the various distributed objects and establish communication between the objects and the client. However, such existing distributed object architectures require that all transactions (communications between client objects and server objects) must pass through an ORB. As a result, the ORB becomes a single failure point which could potentially disable such a system. Further, an ORB typically requires a large amount of memory. Thus, architectures such as CORBA would be unsuitable for "thin" (simple) appliances which have a limited amount of memory.

Consequently, it would be advantageous to develop an information appliance management system employing a standard appliance system architecture. Such an information appliance management system would provide greater fault tolerance than conventional object based architectures, and may be implemented on thin appliances having a limited amount of memory. The information appliance management system would allow management of transactions performed through information appliances.

Additionally, it may be desirable to provide a mechanism to ensure near-optimal execution performance by incorporating a dynamic "critical section" load-time compiler within the execution environment. Thus, the transfer and utilization of objects may be accomplished in a more efficient manner.

Summary of the Invention

The present invention provides a novel execution environment for optimizing the efficiency of a distributed object system. In one embodiment, the invention includes a load-time compiler with ability to interpret, just-in-time compile or pre-compile any object of the distributed object system. In an exemplary embodiment utilizing an object based program environment, an object may be provided in a tagged file format allowing a load-time compiler to identify critical sections of the object code for immediate one-time compiles while delaying compiling of the non-tagged, non-critical code until actually required by the system.

In another aspect of the present invention, a method of dynamic compiling includes loading byte-code on a digital information appliance and identifying a tagged section of the byte-code. The tagged section of byte-code is compiled wherein the tagged section is compiled when the byte-code is loaded so as to enable the digital information appliance to utilize the tagged section of byte-code without additional compiling of the tagged section of byte-code by the digital information appliance.

In a further aspect of the present invention, a system for providing an execution environment that is suitable for dynamic compiling includes a memory device suitable for storing computer readable information and a loader coupled to the memory device, the loader suitable for loading byte-code to the memory. An identifier is coupled to the loader, the identifier suitable for identifying a tagged section of the byte-code. A compiler is coupled to the identifier wherein the identified tagged section is compiled by the compiler when the byte-code is loaded so as to enable the tagged section of byte-code to be utilized without additional compiling of the tagged section of byte-code.

It is to be understood that both the forgoing general description and the following detailed description are exemplary and explanatory only and are not restrictive of the invention as claimed. The accompanying drawings, which are incorporated in and constitute a part of the specification, illustrate embodiments of the invention and together with the general description, serve to explain the principles of the invention.

Brief Description of the Drawings

The numerous advantages of the present invention may be better understood by those skilled in the art by reference to the accompanying figures in which:

5 FIG. 1 is a block diagram illustrating a network of information appliances having a local and a global portion operated at least partially by the architecture of the present invention;

10 FIG. 2 is a block diagram illustrating content exchange between computers and information appliances over a network at least partially operated by the architecture of the present invention;

FIG. 3 is a block diagram illustrating the hierarchy of the dynamic objects which operate within the architecture of the scalable, distributed network of the present invention;

15 FIG. 4 is a block diagram illustrating the relationship between both implementation-dynamic-base-objects (hereinafter "implementation-DBO") and interface-dynamic-base-objects (hereinafter "interface-DBO") operating within the language neutral architecture of the scalable, distributed network of the present invention;

20 FIG. 5 is a flow diagram illustrating the operation of interface-DBOs and implementation-DBOs for providing architecture features and capabilities within the architecture of the scalable, distributed network of the present invention;

25 FIG. 6A is a block diagram illustrating architecture administrator (AA) control over the creation and utilization of implementation-DBOs for providing architecture features and capabilities within the architecture of the scalable, distributed network of the present invention;

FIG. 6B is a flow diagram illustrating architecture administrator (AA) control over the creation and utilization of implementation-DBOs for providing architecture features and capabilities within the architecture of the scalable, distributed network of the present invention;

FIG. 7 is a flow diagram illustrating resource reallocation and DBO termination within the architecture of the scalable, distributed network of the present invention;

FIG. 8 is a flow diagram illustrating communication between a remote implementation-DBO and a local interface-DBO for providing architecture features and capabilities within the architecture of the scalable, distributed network of the present invention;

5 FIG. 9 is a block diagram illustrating scalable resource sharing between, for example, a "thin" information appliance within the architecture of the scalable, distributed network of the present invention;

10 FIG. 10 is a block diagram illustrating scalable resource sharing between, for example, an information appliance within the architecture of the scalable, distributed network of the present invention;

15 FIG. 11 is a block diagram illustrating a software execution model of the architecture of the scalable, distributed network of the present invention;

20 FIG. 12 is a block diagram illustrating an exemplary embodiment of a tagged string or module format in the execution environment of the architecture of the present invention; and

25 FIG. 13 is a flow diagram of an exemplary embodiment of the present invention wherein a dynamic section compiler is utilized in an object programming environment.

Detailed Description of the Invention

The present invention includes a system architecture and operating environment for digital information appliances (DIAs) which allows for feature and feature enhancements for digital information appliances and the like. A DIA is any electronic device capable of operating on a computer network in batch or real-time. Most DIA's include an I/O, a ROM, and a memory. DIAs include both single feature and multiple feature devices. In a preferred embodiment, DIAs operate in the network of the present environment with general purpose computers and the like (FIG. 1).

Referring generally now to FIGS. 1 through 10, a system architecture and operating environment for digital information appliances (DIAs) which allows for feature and feature

enhancements for digital information appliances and the like is shown. A DIA is any electronic device capable of operating on a computer network in batch or real-time. Most DIA's include an I/O, a ROM, and a memory. DIAs include both single feature and multiple feature devices, such as information handling systems. In a preferred embodiment, DIAs operate in the network of the present environment with general purpose computers and the like (FIG. 1).

System Architecture and Operating Environment

To best understand the many novel and innovative features of the universal information appliance management system of the present invention, a discussion of an exemplary underlying system architecture and operating environment is in order. While the patentable features of the present system architecture and operating environment (as claimed herein) will be apparent, other object based or procedural architectures may be utilized to implement the information appliance management system of the present invention.

An object based implementation is described in the preferred embodiment, however those skilled in the art will recognize that the architecture, including a functional hierarchy and an administration function, could be implemented in a procedural implementation without departing from the spirit of the invention.

The system architecture and operating environment of the present invention (herein after "the architecture") includes an object hierarchy and object administrator. Together the object hierarchy and object administrator provide additional services not offered by the underlying operating system. The architecture of the present invention creates a scalable, object driven software architecture that supports both simple appliances, network computers/devices and general purpose computers such as personal computers, servers, "mainframe" computers, and "super" computers (FIG. 2).

The architecture of the present invention supports the creation of compelling and easy-to-use consumer and desktop user-interfaces. Additionally, networking within the architecture of the present invention is pervasive, i.e., resources on the network behave as

local resources and execution is transportable across network boundaries.

Dynamic Base-Objects

The architecture of the present invention also enables efficient development of applications; whether work processors (e.g., word processors), video applications, games or soft appliances. The architecture of the present invention includes dynamic base-objects (DBO). Each DBO implements a defined behavior, but may in addition request and use capabilities of another DBO. DBOs may also provide services to another object such as a DBO requesting another DBO.

In a presently preferred embodiment of the invention a DBO may provide service routines to manage identification and communication with other DBOs. The architecture of the present invention also provides a DBO hierarchy, wherein each DBO or class within the hierarchy specializes in providing one particular type of service. A presently preferred exemplary embodiment of this hierarchy is illustrated in FIG. 3. The hierarchy of the present invention allows for features and capabilities not found in prior art object oriented programming.

In an exemplary embodiment of the architecture of the present invention, when an application creates a DBO, two DBOs are actually created. These two DBOs are an interface-DBO within the application, and an instance of the real DBO (a/k/a an implementation-DBO). This relationship is best illustrated in FIG. 4. In a preferred embodiment of the invention, each time the application uses the interface-DBO, a message is sent to the implementation-DBO, which carries out the task and returns the result, as shown in FIG. 5. When the application frees the DBO the reverse happens. The implementation-DBO gets a message call to de-allocate its resources and terminate.

In an exemplary embodiment of the present invention the hierarchy of the present invention allows the polymorphic and inheritance features of object oriented programming to be more fully realized. For example, in the present invention polymorphism (which allows a routine in a derived class to be redefined), and inheritance (which allows for the

derivation of desired characteristics within a subclass) operate to produce object construction, implementation, and utilization without centralized control, i.e., the object hierarchy of the objects of the present invention manage object construction, implementation, and utilization.

A DBO may be either memory or disk resident. A DBO required for execution is loaded from disk if not present in memory. In a preferred embodiment, DBOs have the following “behavioral” characteristics: (1) capability or feature may be dynamically created, added and changed; (2) other objects including other DBOs may provide a DBO with additional capabilities or features; (3) self checking mechanism with dynamic re-start and re-initialization upon run-time or like failure (4) standardized communication and services interface (e.g., object-to-object, user-to-object, and object-to-user); and (5) fully thread-safe.

The Architecture Administrator

In an exemplary embodiment of the architecture of the present invention when an application creates a DBO, two DBOs are actually created (FIGS. 6A and 6B). These two DBOs are an interface-DBO within the application, and an instance of the real DBO (a/k/a an implementation-DBO). This relationship is best illustrated in FIG. 4. For example the application may create a DBO:

```
DynamicObject *pDObj = new DynamicObject;
```

The “new” call issues a message call to the architecture administrator (AA), which creates a new DBO with the desired characteristics. The object (DBO) created by the AA also contains the implementation. The AA then replies with “identification information” to the implementation-DBO, such that the interface-DBO and DBO (implementation-DBO) may communicate (FIGS. 6A and 6B).

In a preferred embodiment of the invention, each time the application uses the interface-DBO, a message is sent to the implementation-DBO, which carries out the task and returns the result. When the application frees the DBO the reverse happens. The implementation-DBO gets a message call to de-allocate its resources and terminate. When

the interfaced object (IO) receives the termination reply it can then safely terminate as well (FIG. 7).

Thus, the AA serves as the central “launch point” (“dispatcher”) for DBOs. While the AA controls launch of DBOs, it is anticipated in a presently preferred embodiment, it will have no further involvement in DBO transactions. This is important, since to do otherwise introduces a “single-point-of-failure” in the system.

When the AA creates a new DBO it may involve loading a disk-based object. The implementation-DBO will often use resources and application programming interfaces (“APIs”) provided by the underlying operating system (“OS”). Such OS resource will often be provided through shared libraries.

This architecture has a number of significant advantages:

1. The application does not embed any knowledge of the DBO; only the interface. This allows DBOs to be updated on “the-fly” such that a DBO may automatically have an application take advantage of the update.
2. The “message bus” can be either interprocess communication (“IPC”) within one system or “over the network” for two separate systems (FIG. 8).
3. The application does not need to have direct access to the resources needed to run a particular DBO. This allows for “very thin” network appliances (e.g., where the appliance has little RAM and/or ROM) (FIG. 8).
4. If the DBO is a transaction object, the implementation object may issue another network call to a service provider (e.g., a DBO service provider or DBOSP) with the particulars of the transaction.
5. The AA is involved in creating the implementation object. All further communication happens directly between the interface object and the implementation object (FIG. 6A and 6B).

DBO Methods

The interface and implementation-DBOs preferably vary in their implementation.

Preferably the interface DBO will be implemented as a pure message pass. For example, in a simple function called “ADD” which adds to two integer values and returns the sum:

Interface DynamicObject:

```
Int      DynamicObject: :Add(int a, int b)
5
{
    int          sum;
    string s1 = Encode (a);
    string s2 = Encode (b);
    string reply;
    send_message (gpid_implementation_object, dynamic.dynamicobject.add,
10
    sl+s2, reply);
    sum = DecodeInt (reply);
    return sum;
}

```

For the implementation object, its min event loop would receive the message and call a function matching the “dynamic.dynamicobject.add” identifier.

Implementation DynamicObject:

```
Int      implementation_add (string msg, string answer)
{
    // Extract values from msg
    int      a=DecodeInt (msg);
    int      b=DecodeInt (msg[4]);
    int      sum = a+b;
    answer = Encode (sum);
}

```

From the applications view, life is significantly simpler. Assuming that the application has a function called “*SomeFunction()*”, it would proceed as follows to use the DynamicObject based *Add* method implemented herein before:

5 *Void SomeFunction()*
 {
 DynamicObject *A*;
 Int *sum*;
 Sum = A.Add (1, 2);
 }
5

10 While more direct methods of addition are available the example string provided several
advantages transparently to the application:

- 15 1. The calculation is redirected to be performed outside the application (FIG. 8).
2. The redirection may include a network jump, i.e., the implementation may
have been running on a different computer.
3. The implementation is shielded from the application, and as long as the
results stay the same, the implementation may be changed, optimized and
updated without application concern.

20 *DBO Examples*

25 The following examples are provided to those of ordinary skill to further illustrate
the power and adaptability of the architecture of the present invention:

- 30 1. We want to log onto a network. The logon object encrypts our password,
negotiates with the server and finally authenticates our account. It returns
with an “OK” signal, that we are now ready to use network resources.
2. We want to watch a pay-per-view movie. The LogOn object works as in 1,
the Billing and Logging objects connects and notifies the content provider
(e.g., cable, theater, or movie company) we have an account.
3. We want to surf the network with our PDA. We load a browser object. Since
we only have 1 MB of RAM on our system, the browser object connects to
the Netscape browser already running on our PC. Through the transparent
networking capabilities of the DBO it seems to us as if the browser is running

on the PDA even though its really running on the PC (FIG. 9). This may allow the storage of account, billing, registration information, and the like seamlessly across the network.

5

Transparent Relocation of DBOs

The interface-DBO serves only as an interface; it contains no state information. Whereas an implementation-DBO contains all state and execution information. Thus, all execution and state information is localized. This total encapsulation allows relocating the *execution* of a DBO to another system.

10

In an exemplary embodiment of the architecture of the invention a local AA may request a remote AA (i.e. running on a different computer) to create an instance of the implementation object, transfer the state and execution information to the remote system, inform the interface of the next server, and terminate itself. Thus, the interface object is not necessarily connected to an implementation object running on a different computer (FIG. 10).

15

The Architecture Orchestrator and Scriptor

20

DBOs may be programmed in at least two different ways: (1) with compiled code, as shown herein before; or (2) through scripting language with run-time object invocation. Since DBOs may declare their capabilities at runtime, this allows for dynamic run-time execution of DBOs. An orchestrator or language processor may provide dynamic identification of all methods and properties of all DBOs. The Orchestrator may automatically support any DBO loaded on the system. The Orchestrator may also work with the AA to identify available DBOs and load and execute them as necessary. The Scriptor may be a few lines or a complete application.

25

Most Script programs will likely be short and tend to focus on the creation and interaction of a modest number of DBOs; for example, small animations, user interface elements and virtual appliances. There are, however, with JAVA_® no limitations on the

complexity of the Scripts which may be employed. The Scriptor is both OS and processor independent. This provides a simple, consistent way to program at both the user-level through the Scriptor and at a program level through DBO interfaces. For example, to spin an object on a desktop, the Scriptor could be as follows:

```
5      //— Rotate an object around (0, 0, 0) in its local coordinates  
     // Until the user clicks on it with a mouse  
     DynamicObject Icon = FindDynamicObject ("demoobject");  
     Icon.Rotate (10, 10, 10, on_desktop, WAIT_EVENT);
```

The two lines do the following: the first line instructs the Orchestrator to find the object, the second line sets the rotation parameters and tells the system to let it run on the desktop (as opposed to its own window), and instructs the object to continue until it receives an external event. If the *Rotate* method had the last parameter as NULL or equivalent, it would just rotate once and return to the Scriptor; instead it launches a new process and then returns.

The following novel features may be operated using the environment described herein and in particular using the exemplary object based environment.

Execution Environment

Referring now to FIG. 11, the architecture and execution model of the present invention provides system independent execution of applications. In this manner, an application may run on multiple platforms even though the platforms may have different processors, different memory subsystems, different graphics subsystems, and the like. In FIG. 11, an application is shown as being encoded in byte-code. Byte-code refers to an encoding of application source code into a processor independent intermediate code form produced by a compiler. This encoding is in an abstract, processor-independent form that cannot be directly executed by most processing systems but is highly suitable for further analysis (e.g., compiler optimization), for processing by interpreters, or for use in generation of binary instruction for an appliance's processing system. It should be appreciated that the "byte-code" nomenclature is not meant to imply a Java byte-code, but refers only to a byte-

order independent processor description. The application is then loaded into memory by a loader.

The application is then validated and certified by a validation and certification utility to ensure that the application complies with the standard definition of the programming language. The architecture includes a loader with the ability to interpret, just-in-time compile, or pre-compile. The execution model also provides a mechanism to ensure near-optimal execution performance by incorporating a dynamic “critical section” load-time compiler within the execution environment. An application 1200 can “mark” or tag key sections in the system independent executable code 1202 (executable) that are performance sensitive. A diagram representing an exemplary marked file format allowing the load time compiler to identify critical sections for one-time compiles is shown in FIG. 12. The marked 1204 sections 1206 (FIG. 12) are identified by a critical section identification utility to be pre-compiled at load time and non-critical sections 1208 which are not identified 1210 may be compiled later. This enables the expert execution engine to call pre-compiled critical sections without the overhead of the JIT compile. Where the entire executable is marked as critical, the entire application will be compiled at load-time. This provides complete (100%) native performance; but may give the perception of slow load times. This perception can be obviated by storing compiled executables in persistent storage. Further, since the JIT compiler is executing natively compiled code, there will be no overhead of “calling out of the environment into the operating system (OS)”.

Referring now to FIG. 13, an exemplary embodiment of the present invention is shown wherein a critical section compiler is utilized in an object programming environment. Source code is encoded to byte-code, which includes encoding of application source code into a processor independent intermediate code form produced by a compiler. Critical sections of the byte-code are tagged for being pre-compiled at load time. Therefore, once the byte-code is loaded by a digital information appliance, the tagged critical sections of the byte-code may be identified by the digital information appliance to be pre-compiled at load time. Thus, when utilized in an object oriented architecture, critical portions of objects may

be pre-loaded by the digital information appliance to increase the efficiency of accessing the object when needed. For example, a user may request a task which requires a specific resource, such as a particular dynamic base object, to be performed by a digital information appliance that is not currently accessible by the digital information appliance. The digital information appliance, utilizing an architecture administrator may locate and/or create the specified dynamic base object, such as loading a stored dynamic base object on the digital information appliance or accessing the dynamic base object over a network. Portions of the dynamic base object may be pre-compiled utilizing the present method to enable the dynamic base object to be run and accessed more efficiently.

Thus, there has been described an object driven software architecture and several process features which together provide for at least all of the advantages stated herein. Although the invention has been described with a certain degree of particularity, it should be recognized that elements thereof may be altered by persons skilled in the art without departing from the spirit and scope of the invention. It is believed that the system and method for just-in-time compiling of the present invention and many of its attendant advantages will be understood by the forgoing description, and it will be apparent that various changes may be made in the form, construction and arrangement of the components thereof without departing from the scope and spirit of the invention or without sacrificing all of its material advantages, the form herein before described being merely an explanatory embodiment thereof. It is the intention of the following claims to encompass and include such changes.

Claims

What is claimed is;

- 1 1. A method for dynamic compiling, comprising:
2 loading byte-code on a digital information appliance, said byte-code suitable for
3 including a tagged section;

4 identifying the tagged section of the byte-code; and
5 compiling the tagged section of byte-code;

6 wherein the tagged section is compiled when the byte-code is loaded so as to enable
7 the digital information appliance to utilize the tagged section of byte-code without additional
8 compiling of the tagged section of byte-code by the digital information appliance.

- 2 2. The method as described in claim 1, further comprising:
3 encoding application source code to byte-code, the byte-code including code in a
4 processor-independent form which is suitable for further analysis; and
5 tagging a section of the byte-code.

3 3. The method as described in claim 2, wherein the byte-code including code in
4 the processor-independent form is suitable for further analysis including at least one of
5 suitable for compiler optimization, suitable for processing by interpreters, and suitable for
 use in generation of binary instruction for the digital information appliance processing
 system.

1 4. The method as described in claim 2, wherein the section of the byte-code is
2 tagged for being performance sensitive.

1 5. The method as described in claim 1, further comprising storing the compiled
2 tagged section of byte-code in persistent storage.

1 6. The method as described in claim 1, wherein loading includes validating that
2 the byte-code conforms with byte-code suitable for utilization by the digital information
3 appliance.

1 7. A digital information appliance suitable for dynamic compiling, comprising:
2 a processor for implementing a program of instructions; and
3 a memory for storing the program of instructions, the program of instructions suitable
4 for configuring the digital information appliance to

5 load byte-code, said byte-code suitable for including a tagged section;
6 identify the tagged section of the byte-code; and
7 compile the tagged section of byte-code;

8 wherein the tagged section is compiled when the byte-code is loaded so as to enable
9 the digital information appliance to utilize the tagged section of byte-code without additional
10 compiling of the tagged section of byte-code by the digital information appliance.

□
1 8. The digital information appliance as described in claim 7, further comprising:
2 encoding application source code to byte-code, the byte-code including code in a
3 processor-independent form which is suitable for further analysis; and
4 tagging a section of the byte-code.

5 9. The digital information appliance as described in claim 8, wherein the byte-
code including code in the processor-independent form is suitable for further analysis
including at least one of suitable for compiler optimization, suitable for processing by
interpreters, and suitable for use in generation of binary instruction for a digital information
appliance's processing system.

1 10. The digital information appliance as described in claim 7, wherein the section
2 of the byte-code is tagged for being performance sensitive.

1 11. The digital information appliance as described in claim 7, further comprising
2 storing the compiled tagged section of byte-code in persistent storage.

1 12. The digital information appliance as described in claim 7, wherein loading
2 includes validating that the byte-code conforms with byte-code suitable for utilization by the
3 digital information appliance.

13. A system for providing an execution environment that is suitable for dynamic compiling, comprising:

a memory device suitable for storing computer readable information;

a loader coupled to the memory device, the loader suitable for loading byte-code to the memory, said byte-code suitable for including a tagged section;

an identifier coupled to the loader, the identifier suitable for identifying the tagged section of the byte-code;

a compiler coupled to the identifier;

wherein the identified tagged section is compiled by the compiler when the byte-code is loaded so as to enable the tagged section of byte-code to be utilized without additional compiling of the tagged section of byte-code.

14. The system as described in claim 13, further comprising:

an encoder for encoding application source code to byte-code, the byte-code including code in a processor-independent form which is suitable for further analysis; and

a tagger for tagging a section of the byte-code.

15. The system as described in claim 14, wherein the byte-code including code in the processor-independent form is suitable for further analysis including at least one of suitable for compiler optimization, suitable for processing by an interpreter, and suitable for use in generation of binary instruction for a processing system.

16. The system as described in claim 14, wherein the section of the byte-code is tagged for being performance sensitive.

17. The system as described in claim 13, wherein the loader includes a validation utility for validating the byte-code conforms with byte-code suitable for utilization by the system.

1 18. A method for providing an execution environment in an information appliance
2 network, comprising:
3 (a) encoding an application source code in a processor independent byte-code;
4 (b) tagging at least some portion of said processor independent byte-code; and
5 (c) compiling at least some portion of said tagged processor independent byte-code.

1 19. The execution environment for an information appliance network of claim 18,
2 further including validating at least some portion of said processor independent byte-code.

1 20. The execution environment for an information appliance network of claim 18,
2 wherein compiling includes identifying the portion of said tagged processor independent
3 byte-code.

DYNAMIC COMPILING

Abstract

The present invention provides a novel execution environment for optimizing the efficiency of the distributed object system. In one embodiment, the invention includes a compiler with ability to interpret, just-in-time compile or pre-compile any object of the distributed object system. An object is provided in a tagged file format that allows a compiler to identify critical sections of the object code for immediate one-time compiles while delaying compiling of the non-tagged, non-critical code until actually required by the system.

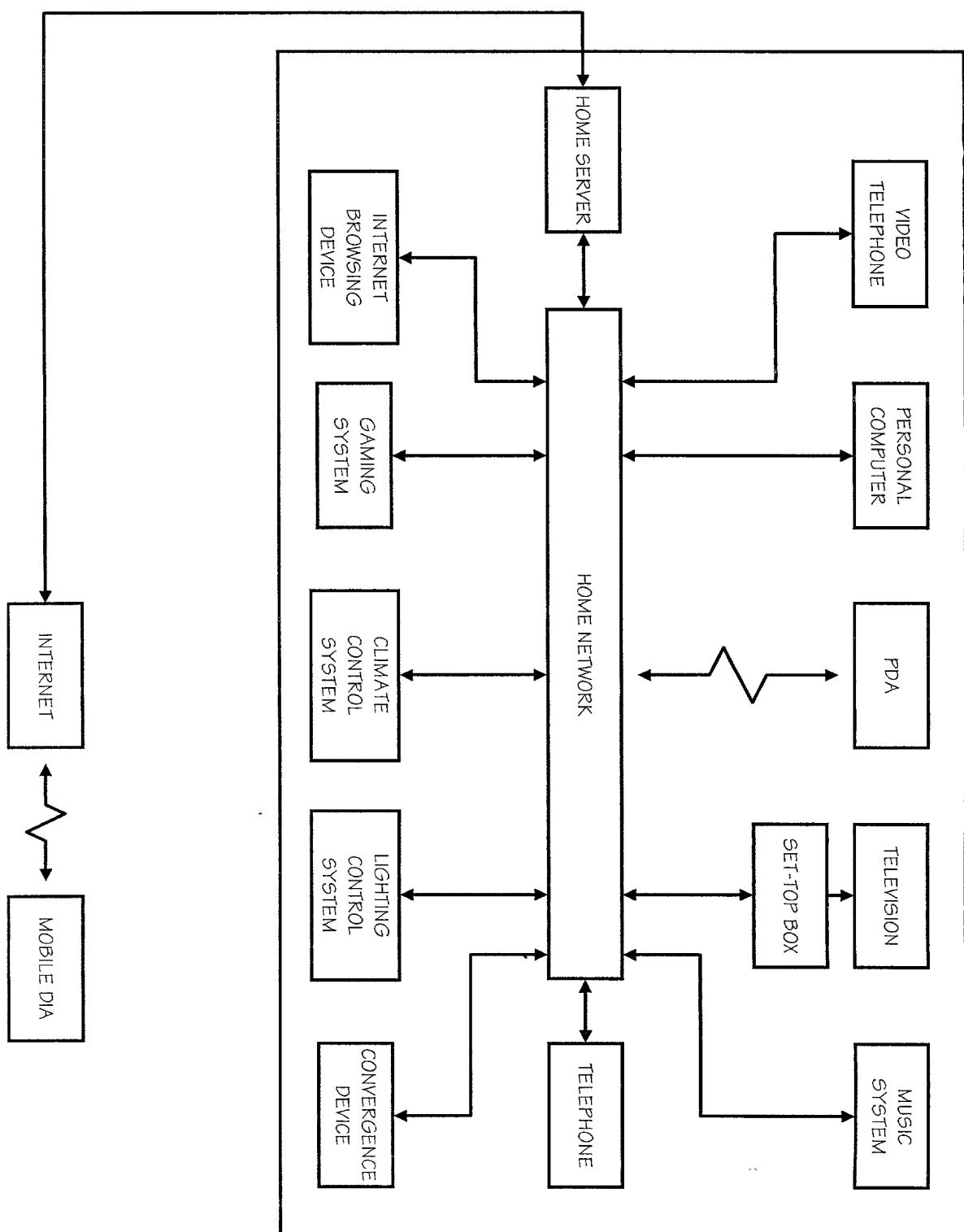


FIG. 1

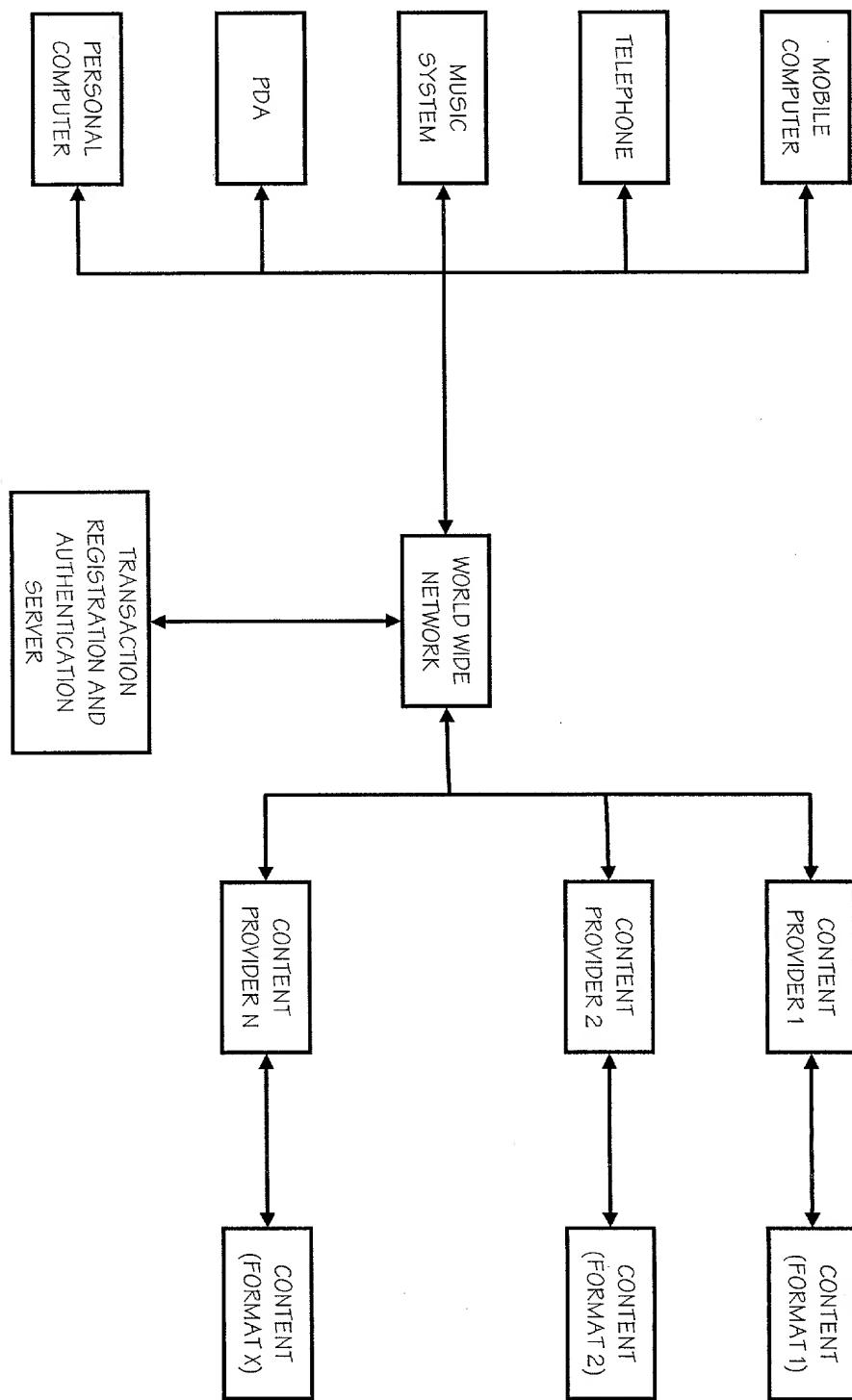


FIG. 2

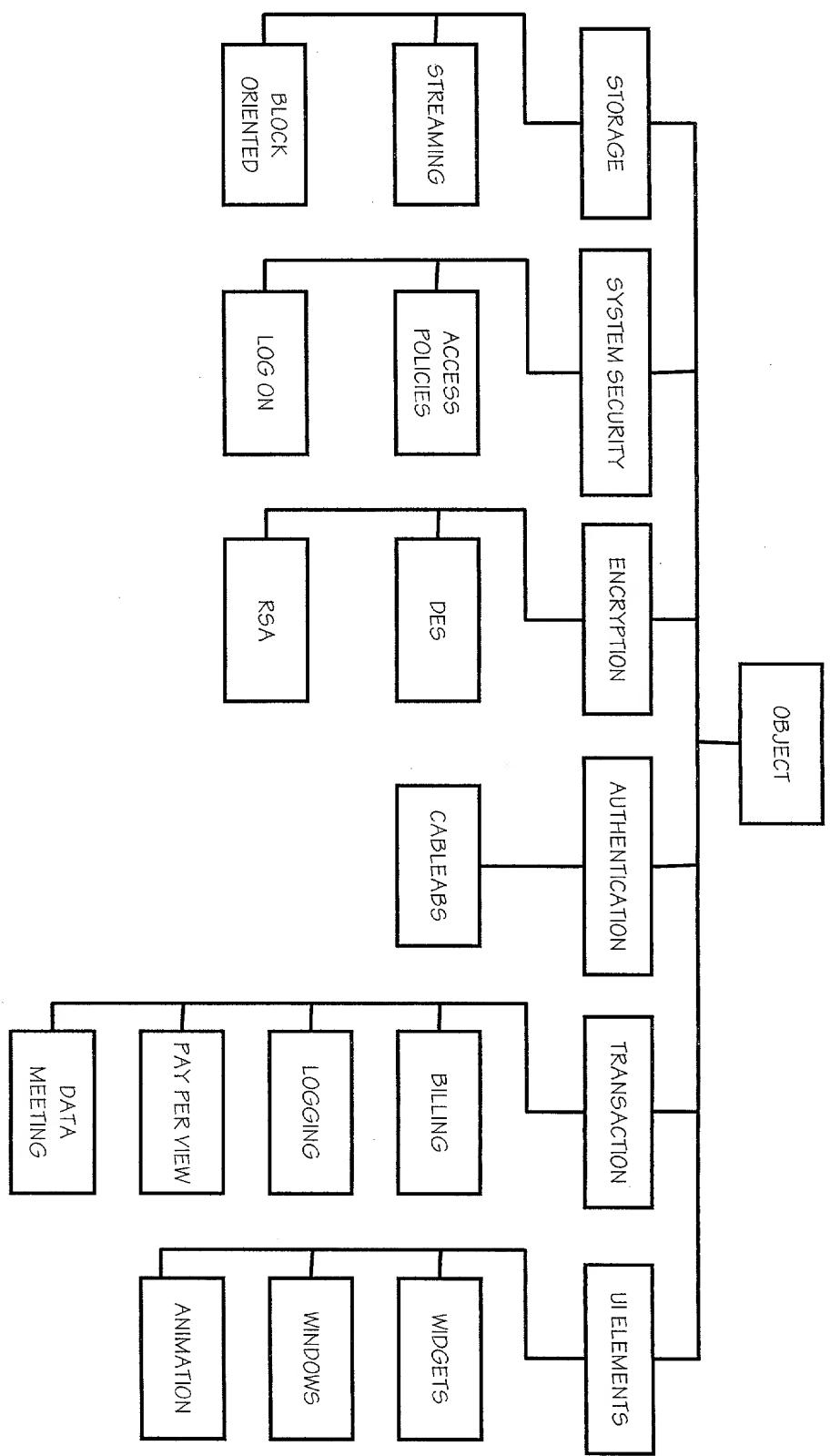


FIG. 3

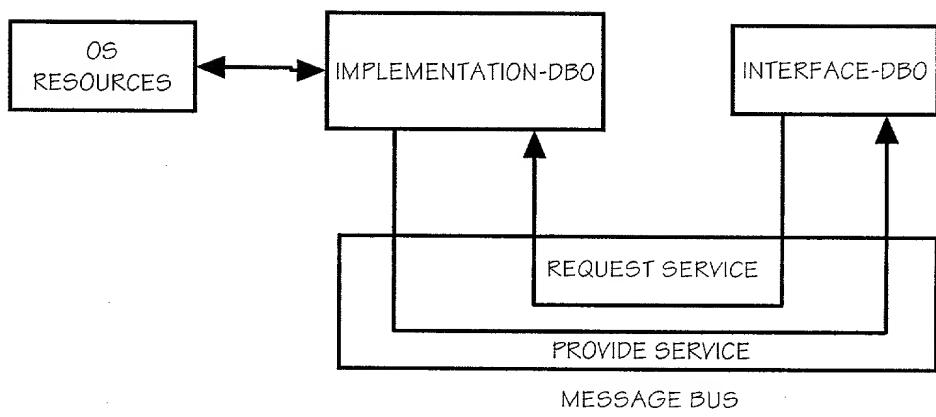


FIG. 4

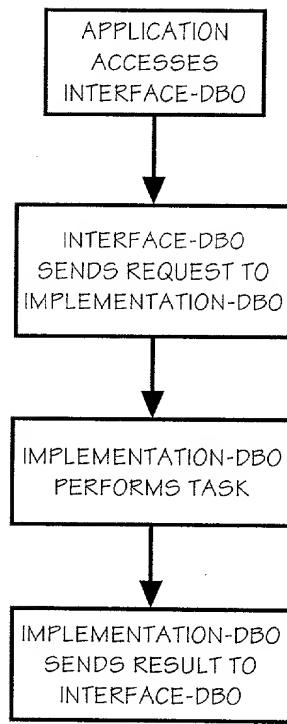


FIG. 5

FIG. 6A

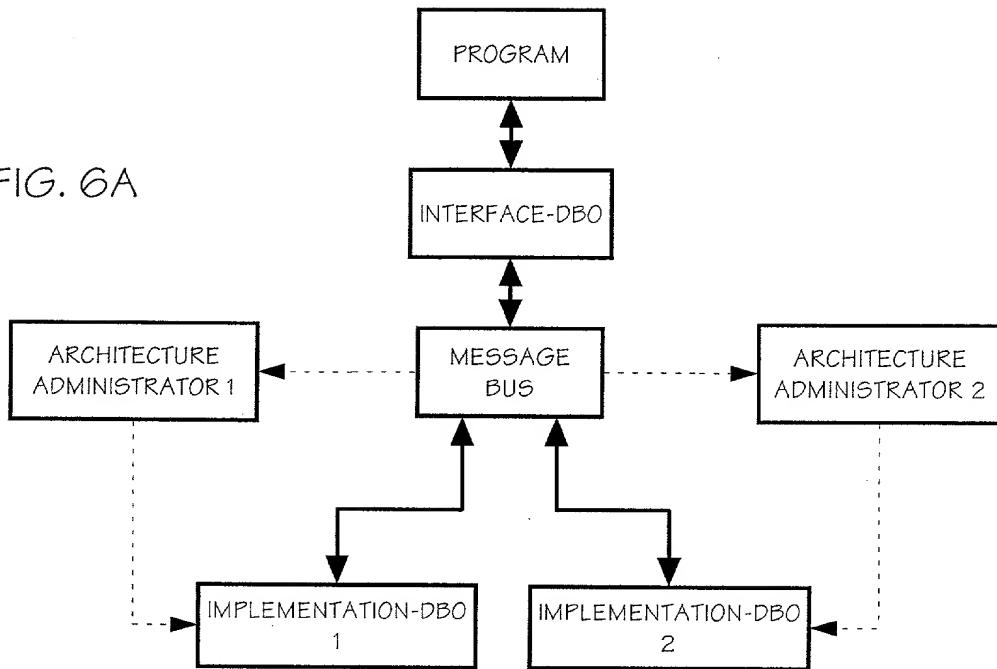
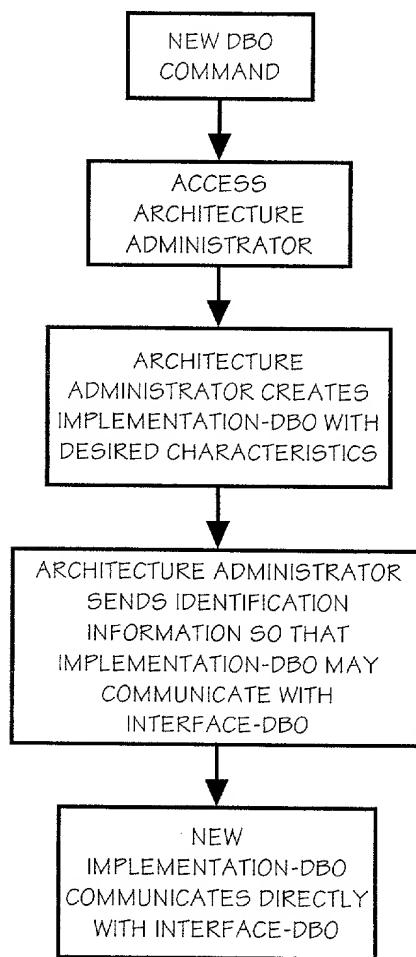


FIG. 6B



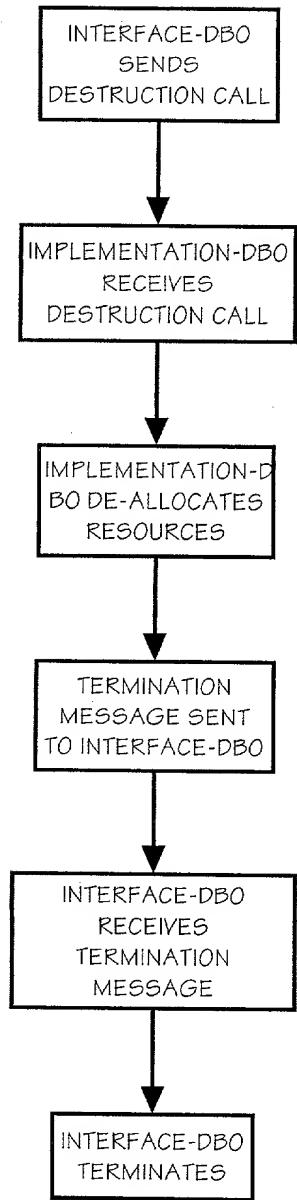


FIG. 7

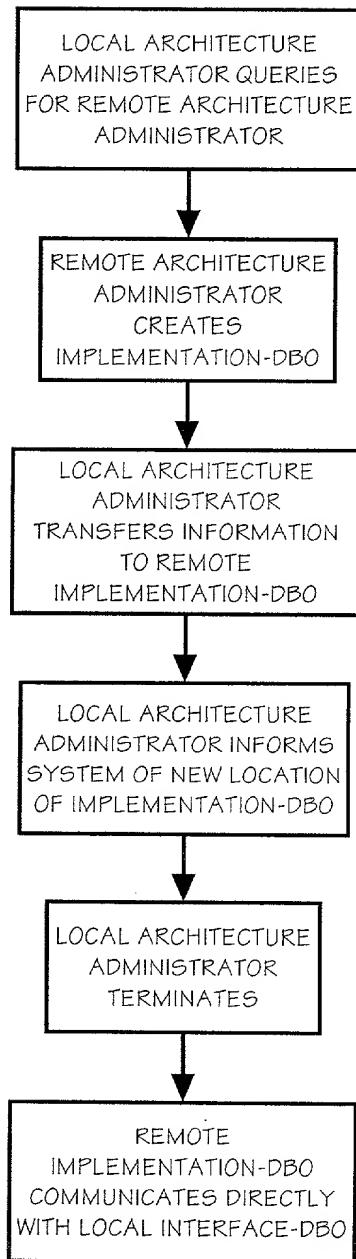


FIG. 8

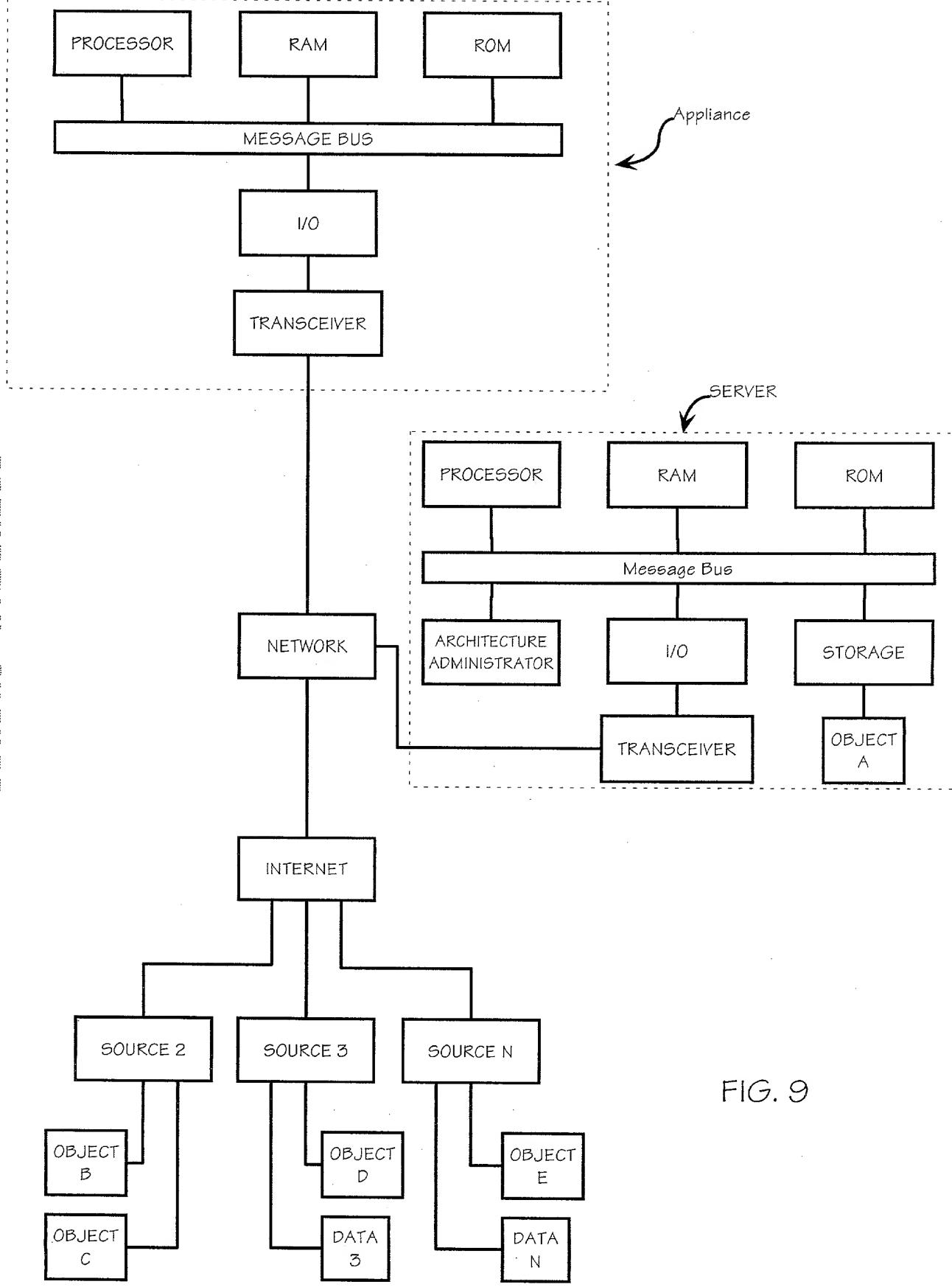


FIG. 9

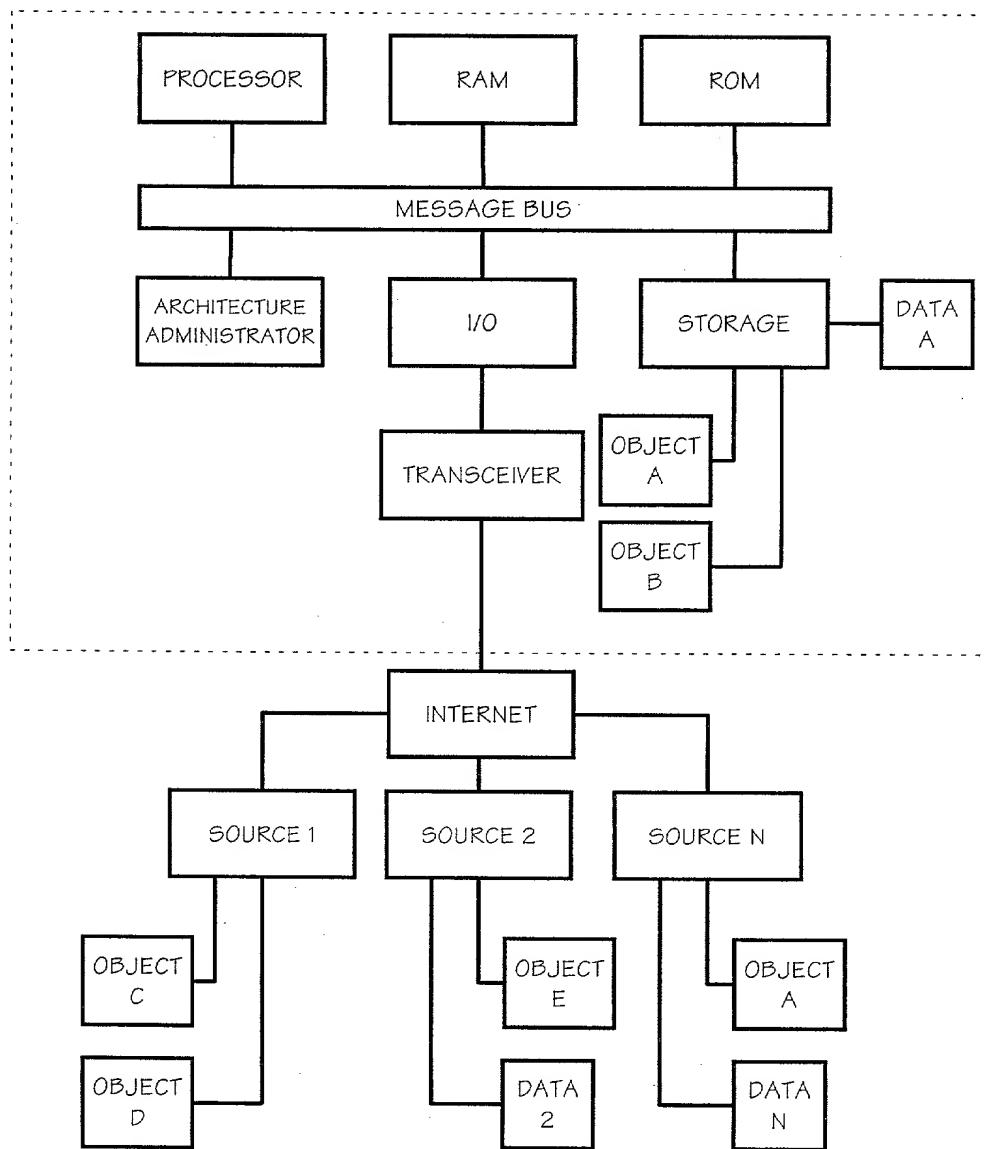


FIG. 10

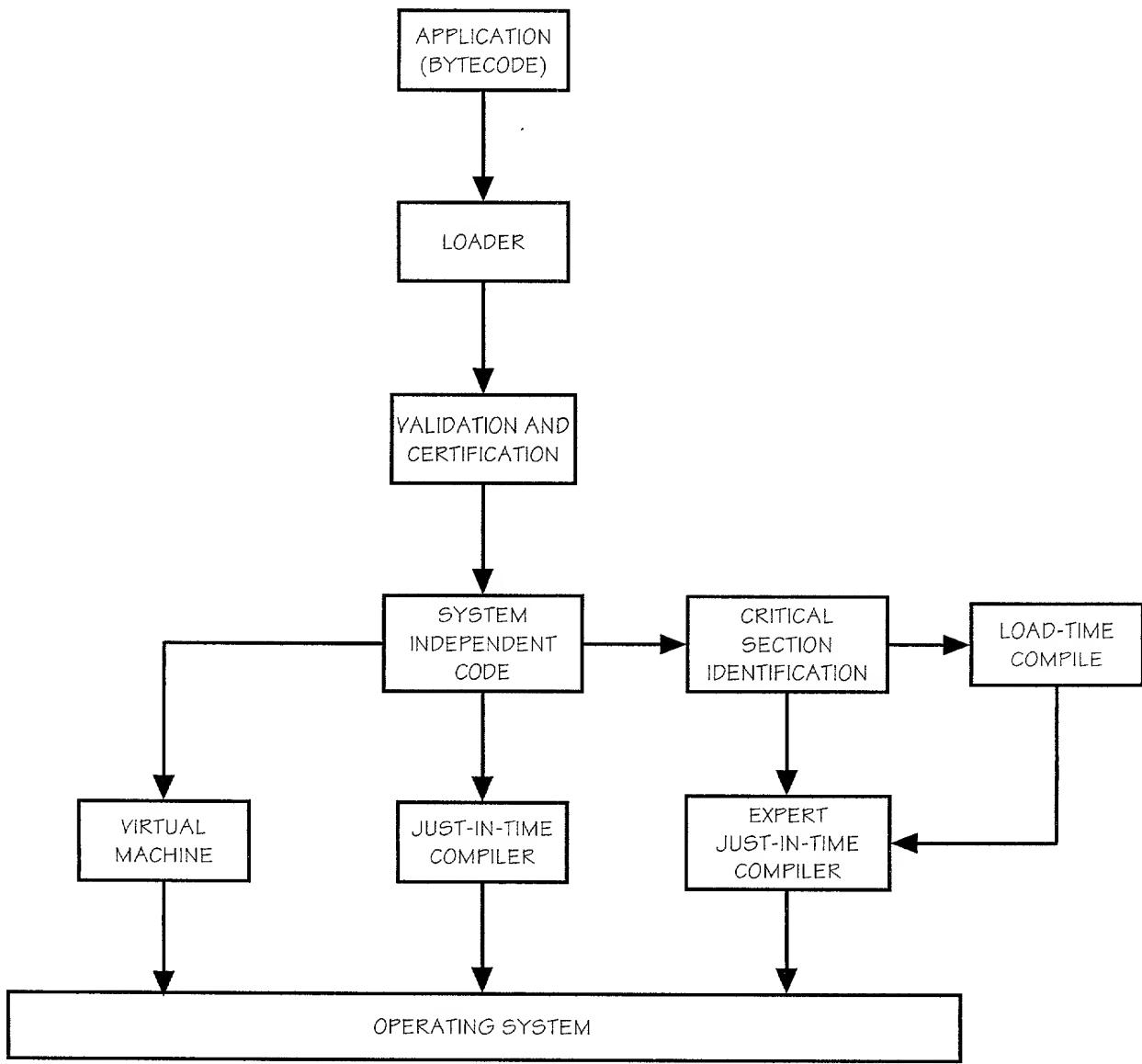


FIG. 11

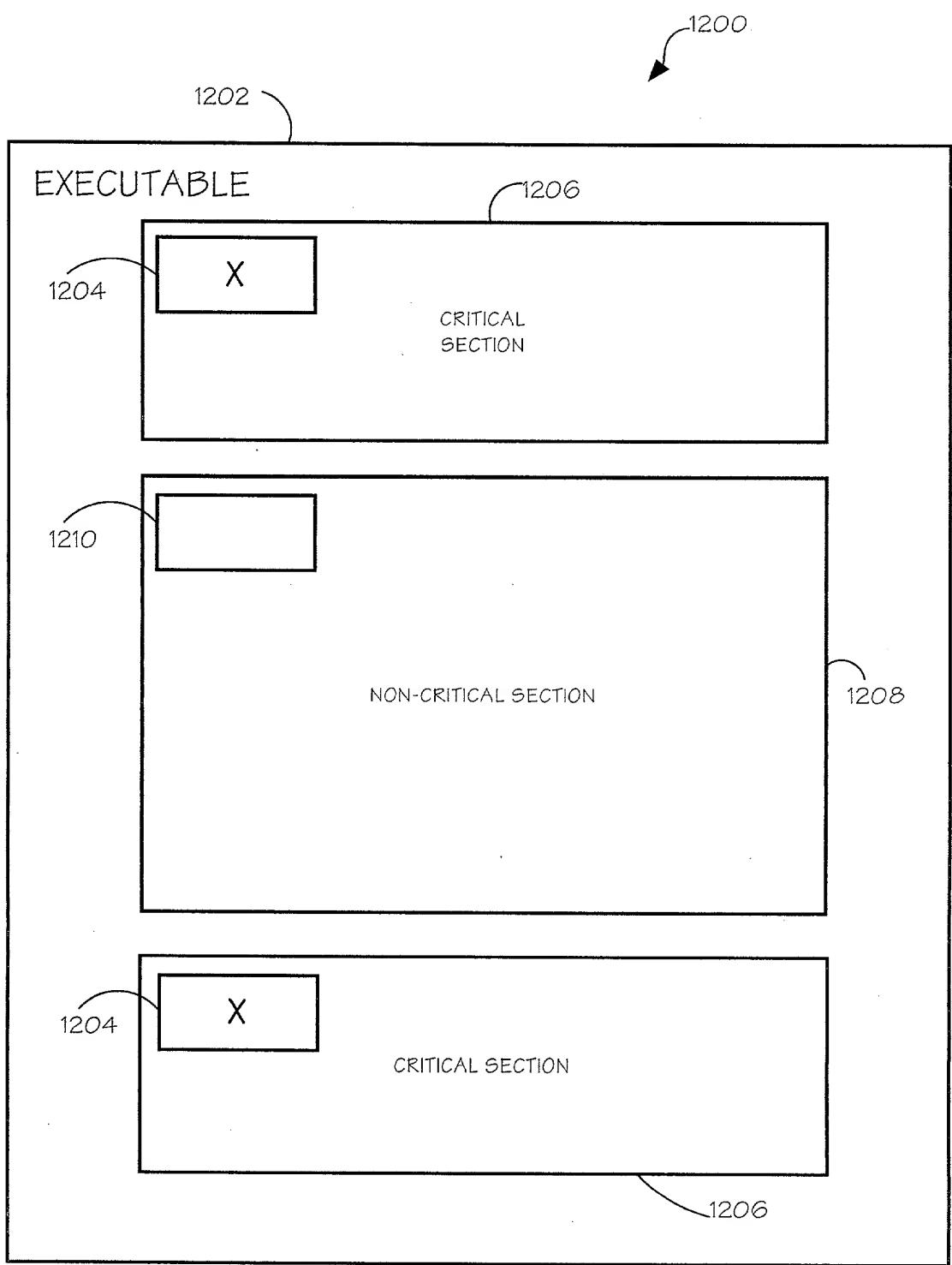


FIG. 12

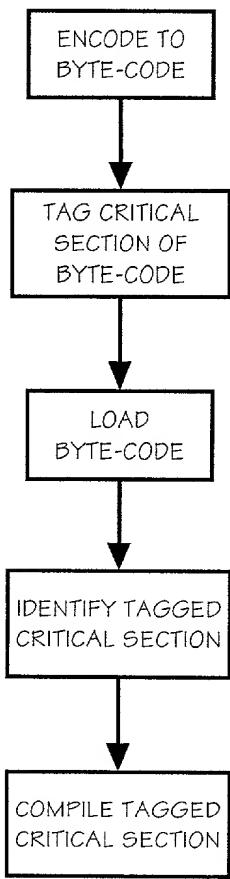


FIG. 13

RULES 63 AND 67 (37 C.F.R. 1.63 and 1.67)
DECLARATION AND POWER OF ATTORNEY

FOR UTILITY/DESIGN/CIP/PCT NATIONAL APPLICATIONS

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; and

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **Dynamic Compiling**, the specification of which:

- (a) is attached hereto.
- (b) was filed on _____ as Application Serial No. _____ and
was amended on _____ (if applicable)
- (c) was filed as PCT International Application No. PCT/_____ on _____ and
was amended on _____ (if applicable).
- (d) was filed on _____ as Application Serial No. _____ and was issued a Notice
of Allowance on _____.
- (e) was filed on _____ and bearing attorney docket number _____.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims as amended by any amendment referred to above or as allowed as indicated above.

I acknowledge the duty to disclose all information known to me to be material to the patentability of this application as defined in 37 CFR § 1.56. If this is a continuation-in-part (CIP) application, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose to the Office all information known to me to be material to patentability of the application as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this CIP application.

I hereby claim foreign priority benefits under 35 U.S.C. § 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate filed by me or my assignee disclosing the subject matter claimed in this application and having a filing date (1) before that of the application on which my priority is claimed or, (2) if no priority is claimed, before the filing date of this application:

PRIOR FOREIGN PATENTS

<u>Number</u>	<u>Country</u>	<u>Month/Day/Year Filed</u>	<u>Date first laid-open or Published</u>	<u>Date patented or Granted</u>	<u>Priority Claimed</u>
—	—	—	—	—	—
—	—	—	—	—	—

I hereby claim the benefit under 35 U.S.C. § 120/365 or § 119 of any United States application(s) listed below and PCT international applications listed above or below:

PRIOR U.S. OR PCT APPLICATIONS

<u>Application No. (series code/serial no.)</u>	<u>Month/Day/Year Filed</u>	<u>Status(pending, abandoned, patented)</u>
60/127,767	April 5, 1999	pending
09/312,123	May 14, 1999	pending

I hereby appoint, Anthony B. Claiborne, Reg. No. 39,636, Mark S. Walker, Reg. No. 30,699, Sean P. Suiter, Reg. No. 34,260, Scott C. Rand, Reg. No. 40,359, Kenneth J. Cool, Reg. No. 40,570, Kevin E. West, Reg. No. 43,983, William J. Breen, III, Reg. No. P45,313 and Chad W. Swantz, Reg. No. P46,329 as my attorneys and/or agents, with full power of substitution and revocation, to prosecute this application, provisionals thereof, continuations, continuations-in-part, divisionals, appeals, reissues, substitutions, and extensions thereof and to transact all business in the United States Patent and Trademark Office connected therewith, to appoint any individuals under an associate power of attorney and to file and prosecute any international patent application filed thereon before any international authorities, and I hereby authorize them to act and rely on instructions from and communicate directly with the person/assignee/attorney/firm/organization who/which first sent this case to them and by whom/which I hereby declare that I have consented after full disclosure to be represented unless/until I instruct them in writing to the contrary.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable

by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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	Post Office Address (include zip code)		
	Full Name	Inventor's Signature	Date
3	Residence (city, state, country)	Citizenship	
	Post Office Address (include zip code)		